

# WL2811EA

**Low noise, High PSRR, High speed, CMOS LDO**

## Descriptions

The WL2811EA series is a high accuracy, low noise, high speed, high PSRR, low dropout CMOS Linear regulator with high ripple rejection. The devices offer a new level of cost effective performance in cellular phones, laptop and notebook computers, and other portable devices.

The WL2811EA has the fold-back maximum output current which depends on the output voltage. So the current limit functions both as a short circuit protection and as an output current limiter.

The WL2811EA regulators are available in standard SOT-23-5L Package. Standard products are Pb-free and Halogen-free.

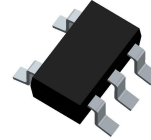
## Features

- Input Voltage Range : 2V~5.5V
- Output Voltage Range : 0.8V~5V
- Output current : 300mA
- Quiescent current : 75µA Typ.
- Shut-down current : < 1µA
- Dropout voltage : 141mV @ I<sub>OUT</sub>=0.3A
- PSRR : 70dB @ 1kHz, V<sub>OUT</sub>=3V
- Low Output Voltage Noise : 12µV<sub>RMS</sub> Typ.
- Output Voltage Tolerance : ±2%
- Recommend capacitor : 1µF
- Thermal-Overload and Short-Circuit Protection

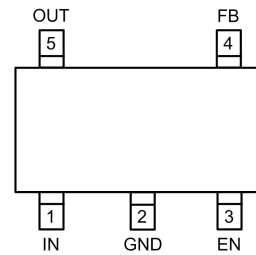
## Applications

- MP3/MP4 Players
- Cellphones, radiophone, digital cameras
- Bluetooth, wireless handsets
- Others portable electronics device

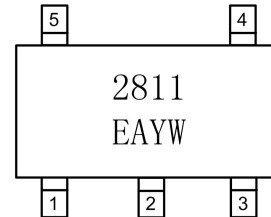
[Http://www.ovt.com](http://www.ovt.com)



**SOT-23-5L**



**Pin Configuration (Top View)**



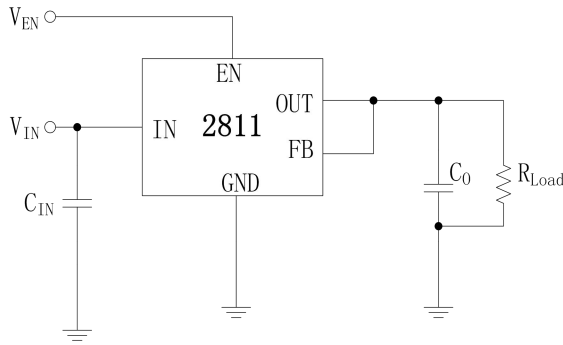
**2811: Device Code**  
**EA : Special Code**  
**Y : Year Code**  
**W: Week Code**

## Marking

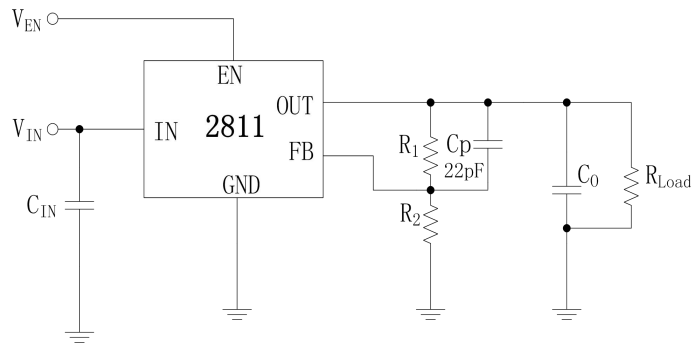
## Order Information

Device	Package	Shipping
WL2811EA-5/TR	SOT-23-5L	3000/Reel&Tape

Typical Application



For  $V_{OUT}=0.8V$  Application



For  $V_{OUT} > 0.8V$  Application

Pin Description  
SOT-23-5L

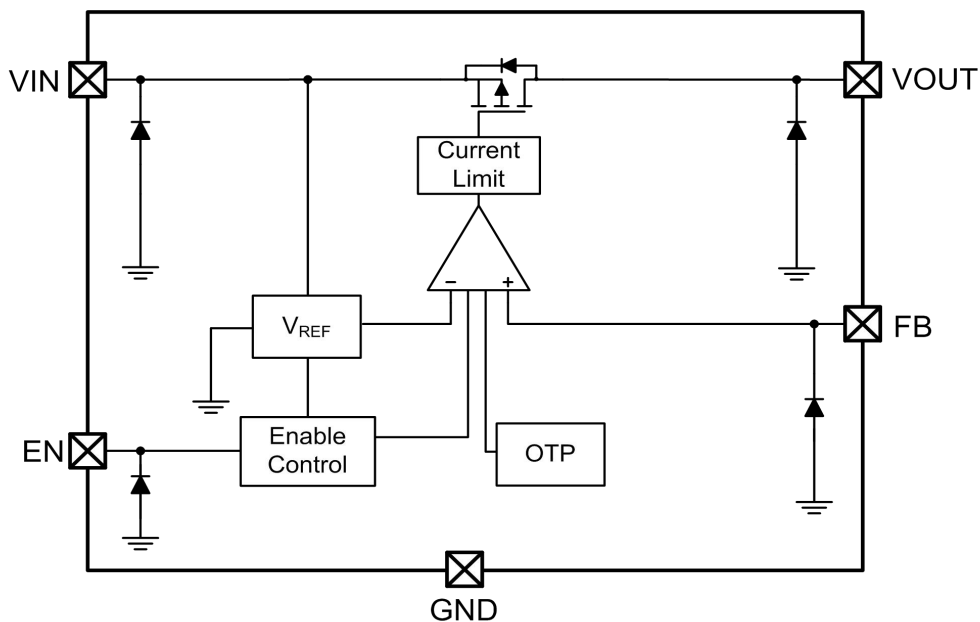
PIN	Symbol	Description
1	IN	Regulator Input .
2	GND	Ground .
3	EN	Enable (Active high).
4	FB	Feedback Pin.This is used to set the output voltage of the device.
5	OUT	Regulator Output .

$$R_1 = R_2 \times \left( \frac{V_{OUT}}{0.8V} - 1 \right)$$

$V_{OUT}(V)$	$R_1(k\Omega)$	$R_2(k\Omega)$
1.0	10.5	40.2
1.8	51.1	40.2
2.85	97.6	37.4
3.0	97.6	35.7

Standard 1% Resistor Values for Common Output Voltages of Adjustable Voltage Version

Block Diagram



### Absolute Maximum Ratings

Parameter	Value	Unit	
V <sub>IN</sub> Range	-0.3~6.5	V	
V <sub>EN</sub> Range	-0.3~V <sub>IN</sub>	V	
V <sub>OUT</sub> Range	-0.3~V <sub>IN</sub>	V	
I <sub>OUT</sub>	300	mA	
Lead Temperature Range	260	°C	
Storage Temperature Range	-55 ~ 150	°C	
Operating Junction Temperature Range	150	°C	
MSL	Level-3		
ESD Ratings	HBM	8000	V
	MM	400	V

### Recommend Operating Ratings

Parameter	Value	Unit
Operating Supply voltage	2~5.5	V
Operating Junction Temperature Range, T <sub>j</sub>	-40~125	°C
Operating Temperature Range	-40~85	°C
Thermal Resistance, R <sub>θJA</sub> (SOT-23-5L), Note1	125	°C/W
Thermal Resistance, R <sub>θJC</sub> (SOT-23-5L)	73	°C/W

Note1. Surface mounted on FR-4 Board using 2 oz, 1 square inch Cu area, PCB board size 1.5\*1.5 square inches.

**Electronics Characteristics**

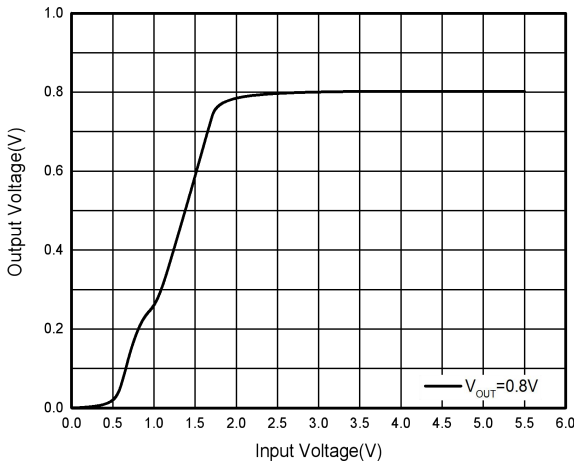
(Ta=25°C, VIN=VOUT+1V, CIN=COUT=1μF, CP=22pF, IOUT=1mA, unless otherwise noted)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	
Feedback Voltage	Vfb	IOUT=1mA	0.8*0.98	0.8	0.8*1.02	V	
Input Voltage	VIN		2		5.5	V	
Current Limit	ILIM	VEN=VIN	300			mA	
Dropout Voltage (IOUT = 300mA) (note)	VDROP	1.8V ≤ VOUT < 2.1V		0.2	0.3	V	
		2.1V ≤ VOUT < 2.5V		0.17	0.26		
		2.5V ≤ VOUT < 2.8V		0.15	0.23		
		2.8V ≤ VOUT		0.14	0.21		
Line Regulation	ΔVLINE	VIN=VOUT+1V~5.5V		1	5.1	mV	
Load Regulation	ΔVLoad	IOUT=1~300mA		1	5.1	mV	
Quiescent Current	IQ	IOUT=0		75	100	μA	
Short Current	ISHORT	VEN=VIN, VOUT Short to GND with 1 Ω		157	280	mA	
Shut-down Current	ISHDN	VEN=0V		0.12	<1	μA	
Power Supply Rejection Rate	PSRR	VIN=(VOUT+1V)DC+0 .5VP-P, IOUT=10mA, VOSSET=3V, Cp=22pF , R2=100K Ω	f=100Hz		70		dB
			f=1kHz		70		dB
			f=10kHz		58		dB
			f=100kHz		45		dB
EN logic high voltage	VENH	VIN=5.5V	1			V	
EN logic low voltage	VENL	VIN=5.5V			0.4	V	
EN Input Current	IEN	VEN=5.5V		0.14		μA	
Output Noise Voltage	eNO	10Hz to 100KHz, COUT=1μF, IOUT=10mA, Vout=0.8V		12		μVRMS	
Thermal shutdown threshold	TSD			166		°C	
Thermal shutdown hysteresis	Δ TSD			35		°C	

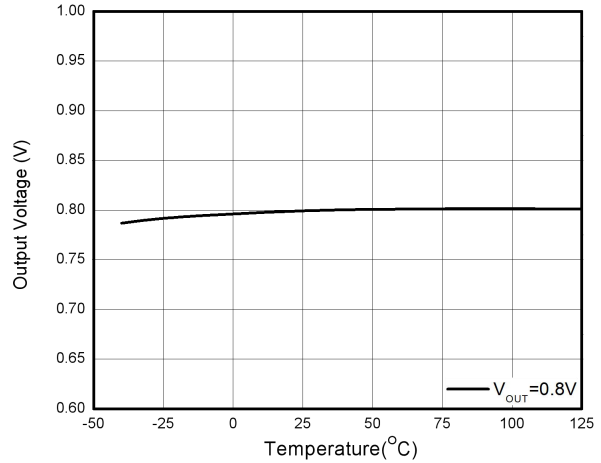
note: When VOUT &lt; 1.8V, VDD should be greater than 2V.

**Typical characteristics** ( $T_a=25^{\circ}\text{C}$ ,  $V_{\text{IN}}=V_{\text{OUT}}+1\text{V}$ ,  $I_{\text{OUT}}=1\text{mA}$ ,  $C_{\text{IN}}=C_{\text{OUT}}=1\ \mu\text{F}$ ,  $\text{CP}=22\text{pF}$ , unless otherwise noted)

**$V_{\text{OUT}}=0.8\text{V}$**

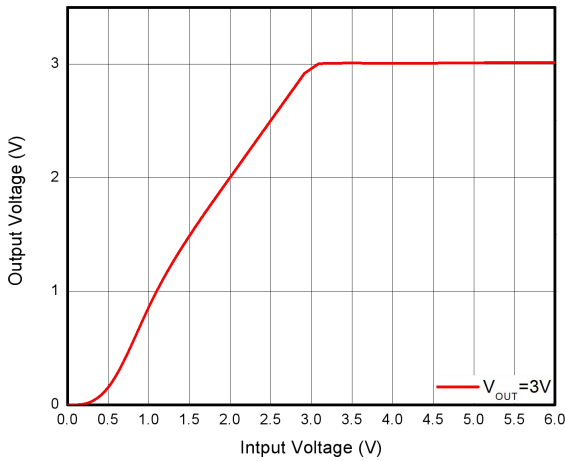


**Output Voltage VS Input Voltage**

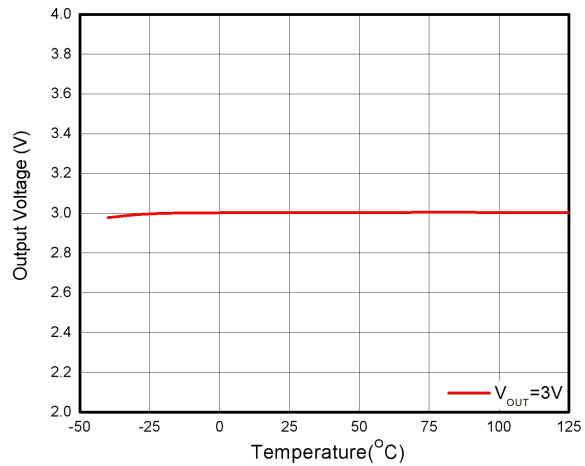


**Output Voltage VS Temperature**

**$V_{\text{OUT}}=3\text{V}$**

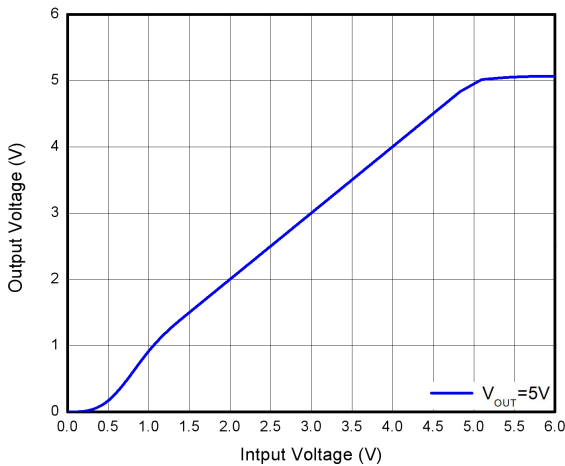


**Output Voltage VS Input Voltage**

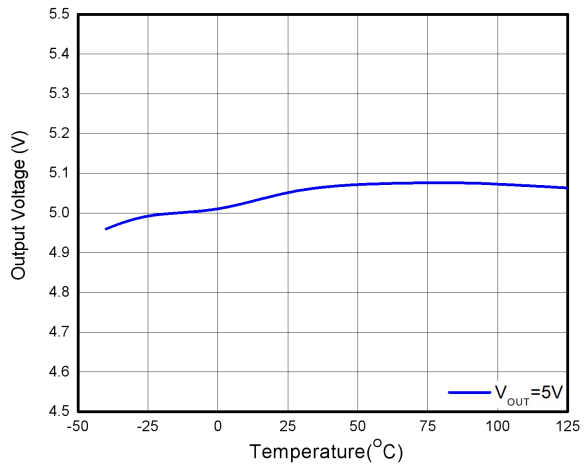


**Output Voltage VS Temperature**

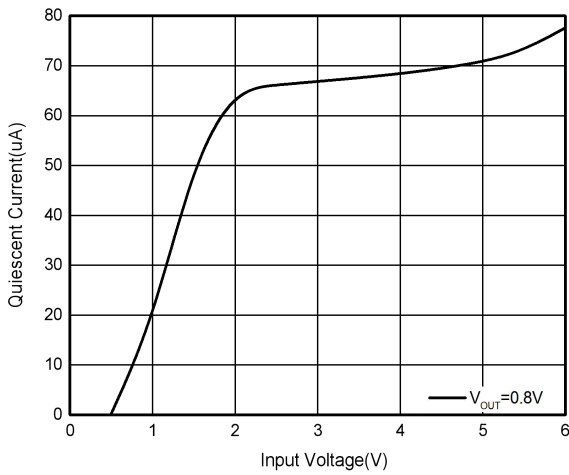
**$V_{\text{OUT}}=5\text{V}$**



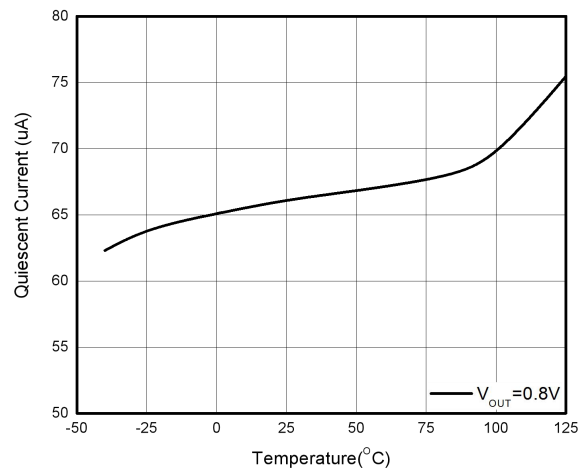
**Output Voltage VS Input Voltage**



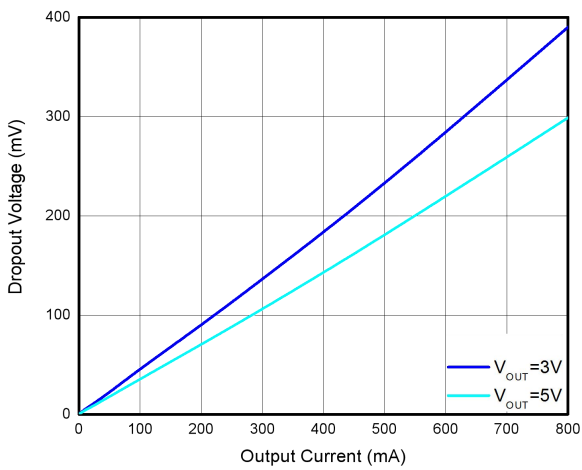
**Output Voltage VS Temperature**



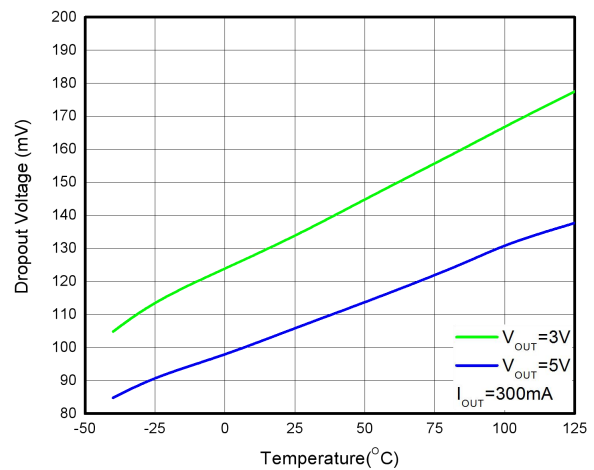
Quiescent Current VS Input Voltage



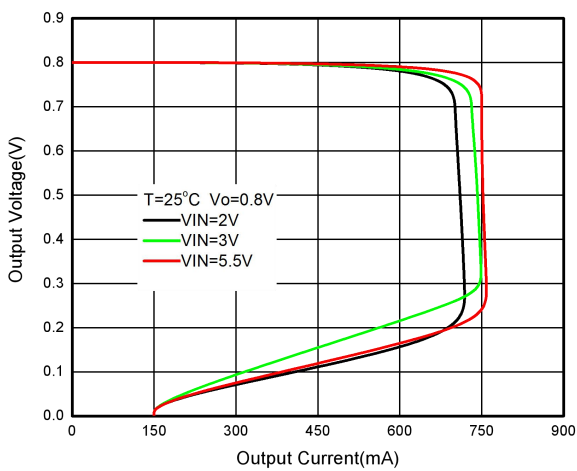
Quiescent Current VS Temperature



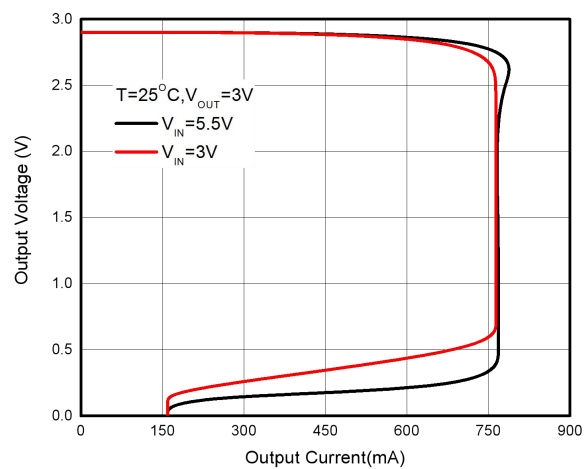
Dropout Voltage VS Output Current



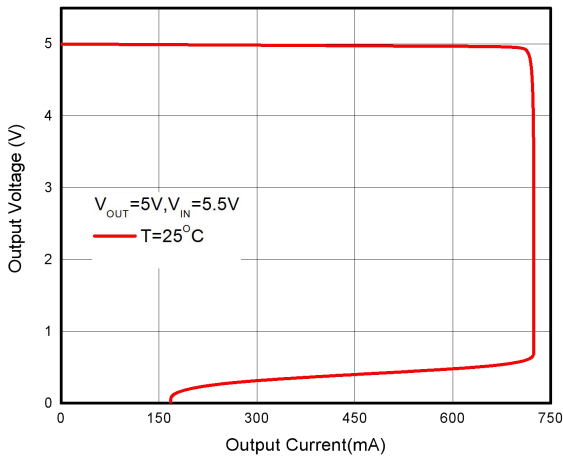
Dropout Voltage VS Temperature



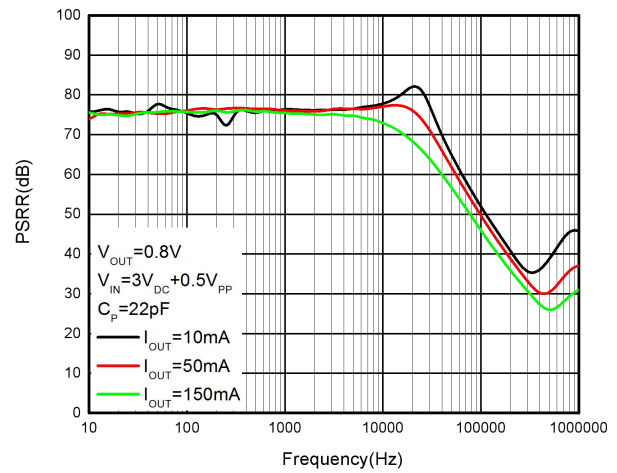
Output Voltage VS Output Current



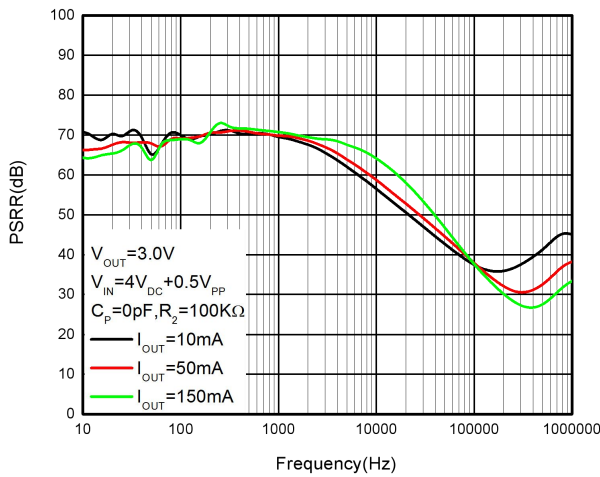
Output Voltage VS Output Current



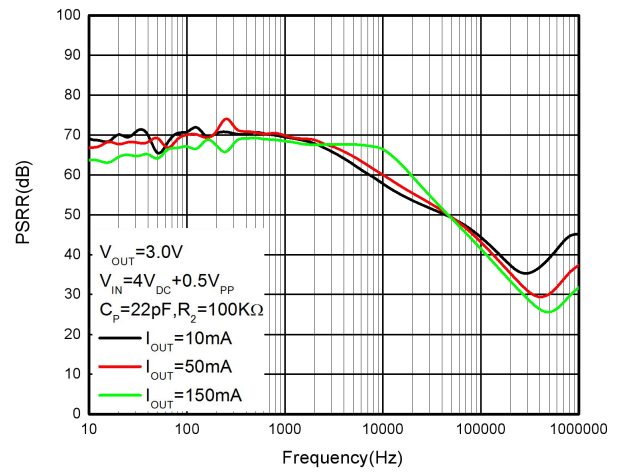
Output Voltage VS Output Current



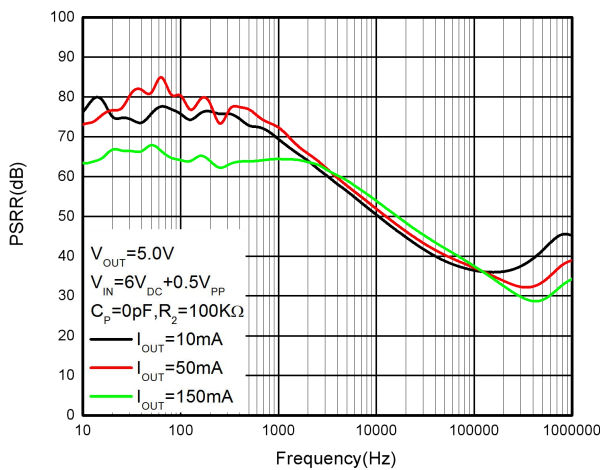
PSRR VS Frequency



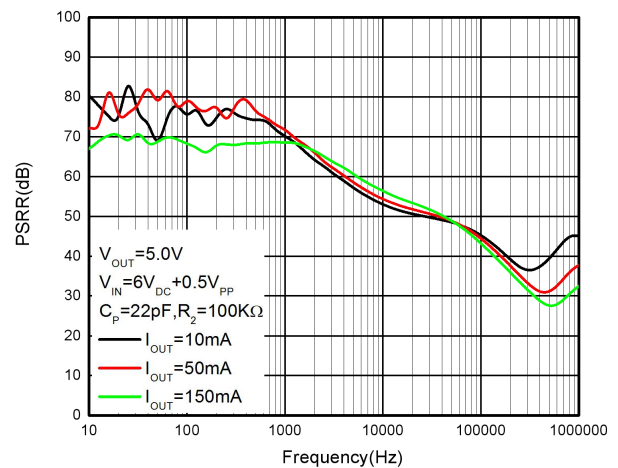
PSRR VS Frequency



PSRR VS Frequency



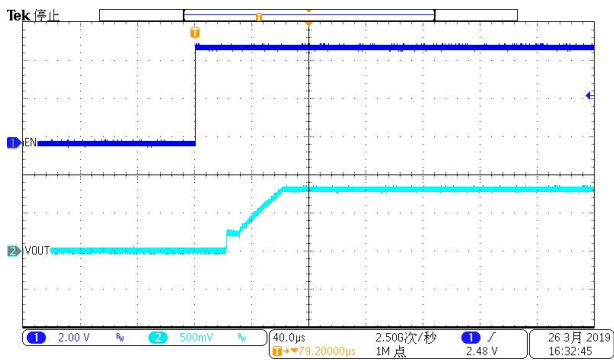
PSRR VS Frequency



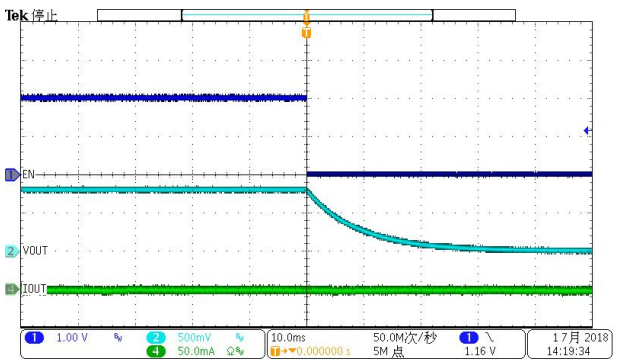
PSRR VS Frequency

### 1. Start up & Shutdown(I<sub>out</sub>=1mA)

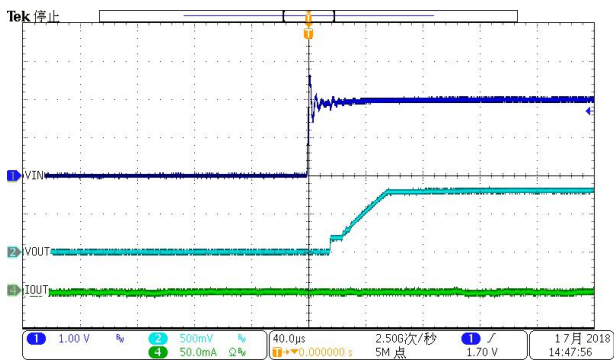
V<sub>OUT</sub>=0.8V,EN ON



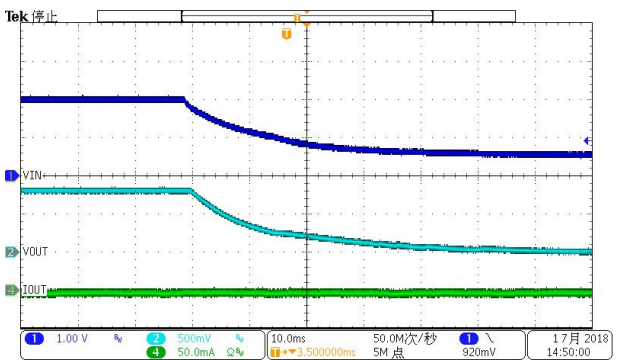
V<sub>OUT</sub>=0.8V,EN OFF



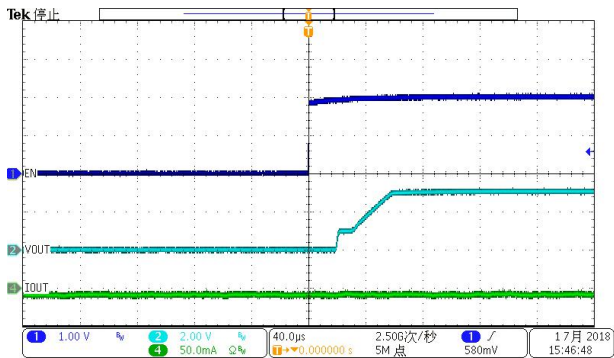
V<sub>OUT</sub>=0.8V,Power ON



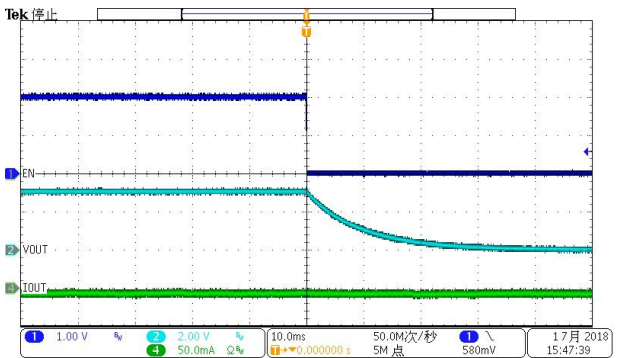
V<sub>OUT</sub>=0.8V,Power OFF



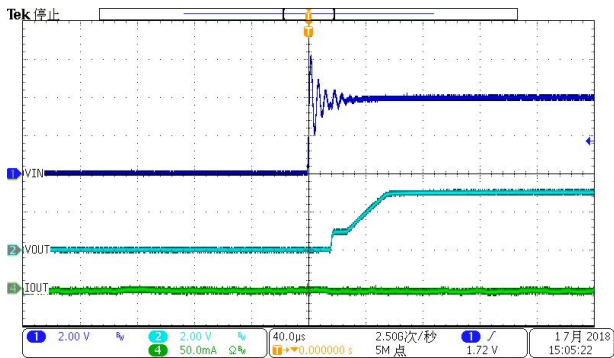
V<sub>OUT</sub>=3V,R<sub>2</sub>=100kΩ,C<sub>P</sub>=0,EN ON



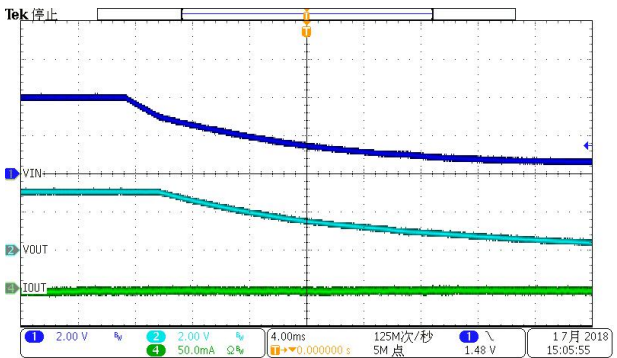
V<sub>OUT</sub>=3V,R<sub>2</sub>=100kΩ,C<sub>P</sub>=0,EN OFF



V<sub>OUT</sub>=3V,R<sub>2</sub>=100kΩ,C<sub>P</sub>=0,Power ON

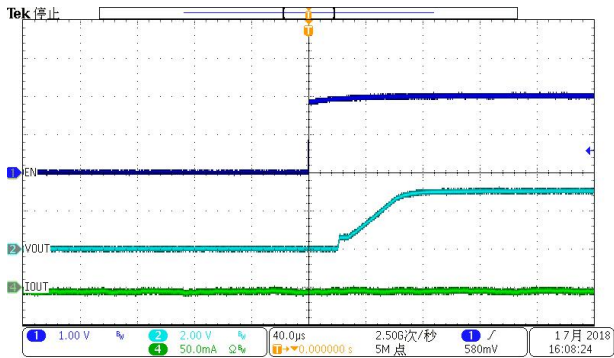


V<sub>OUT</sub>=3V,R<sub>2</sub>=100kΩ,C<sub>P</sub>=0,Power OFF

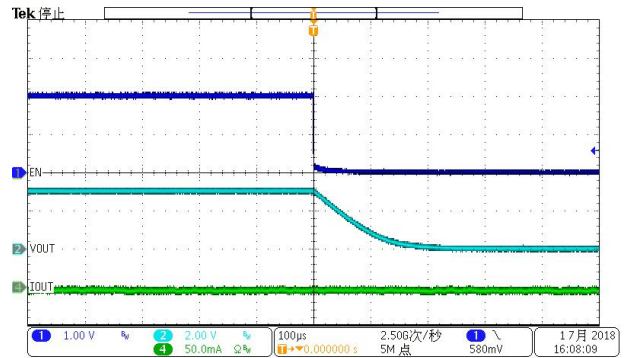




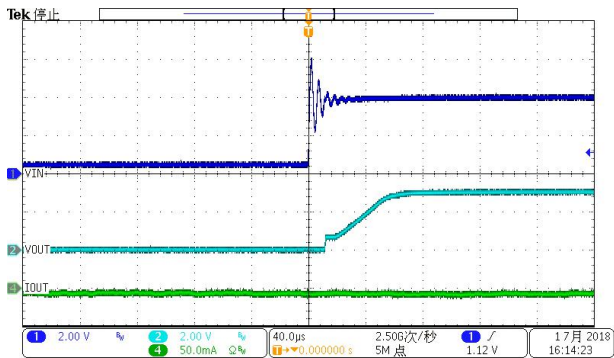
$V_{OUT}=3V, R_2=100k\Omega, C_P=22pF, EN\ ON$



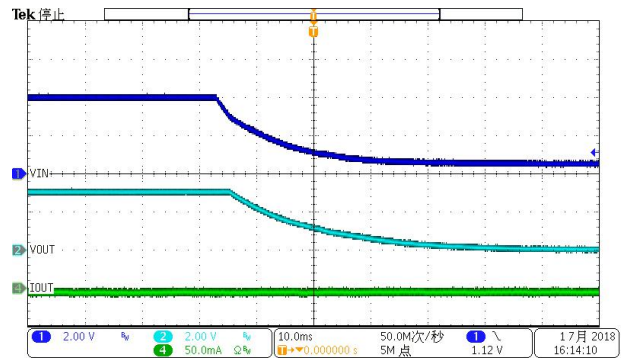
$V_{OUT}=3V, R_2=100k\Omega, C_P=22pF, EN\ OFF$



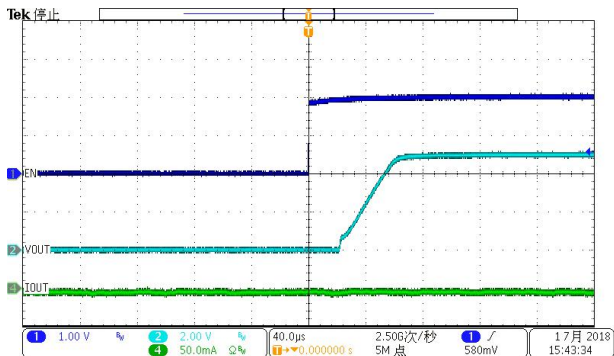
$V_{OUT}=3V, R_2=100k\Omega, C_P=22pF, Power\ ON$



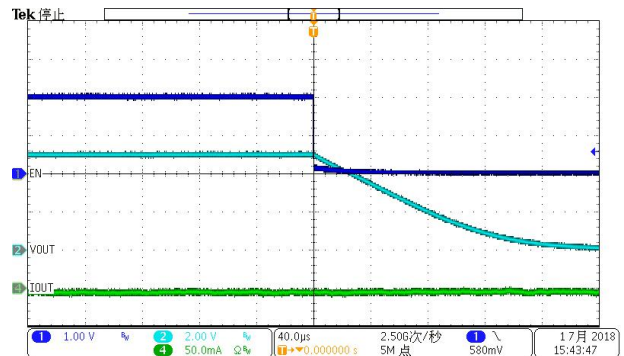
$V_{OUT}=3V, R_2=100k\Omega, C_P=22pF, Power\ OFF$



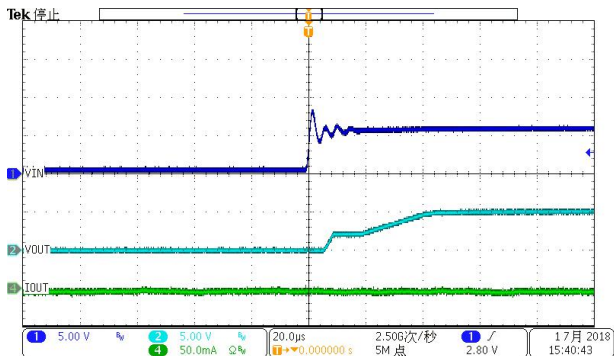
$V_{OUT}=5V, R_2=100k\Omega, C_P=0, EN\ ON$



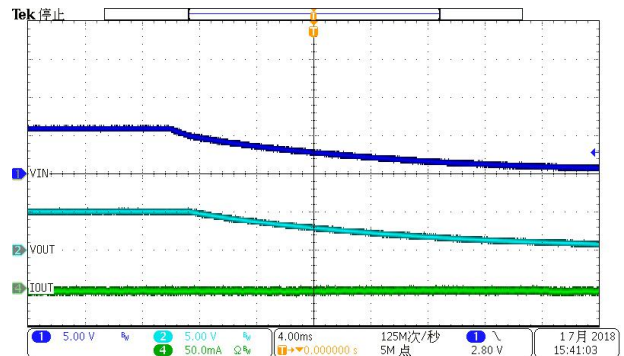
$V_{OUT}=5V, R_2=100k\Omega, C_P=0, EN\ OFF$



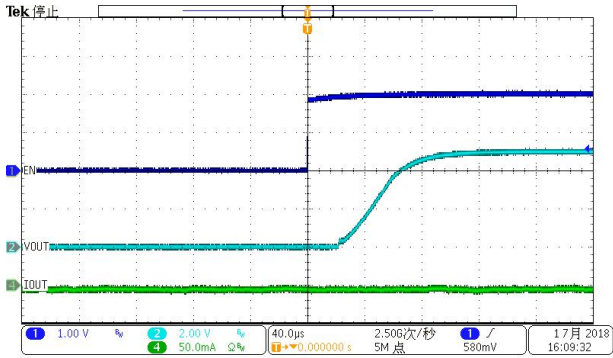
$V_{OUT}=5V, R_2=100k\Omega, C_P=0, Power\ ON$



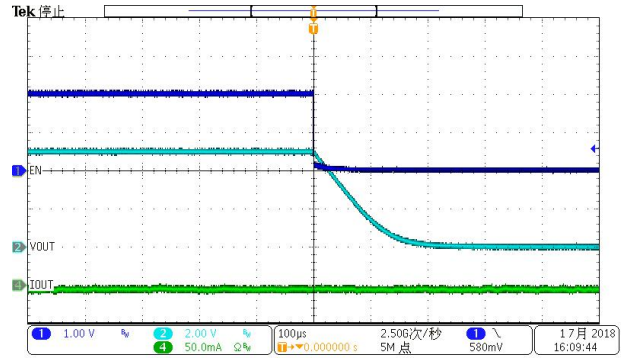
$V_{OUT}=5V, R_2=100k\Omega, C_P=0, Power\ OFF$



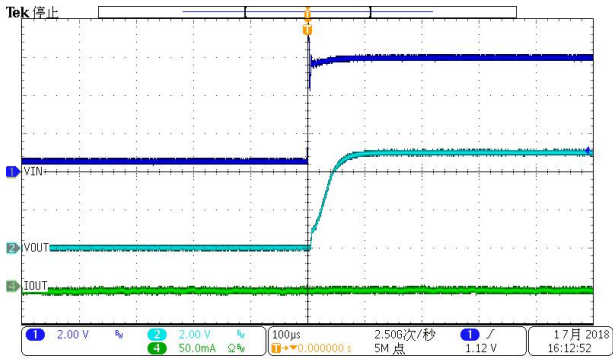
$V_{OUT}=5V, R_2=100k\Omega, C_P=22pF, EN\ ON$



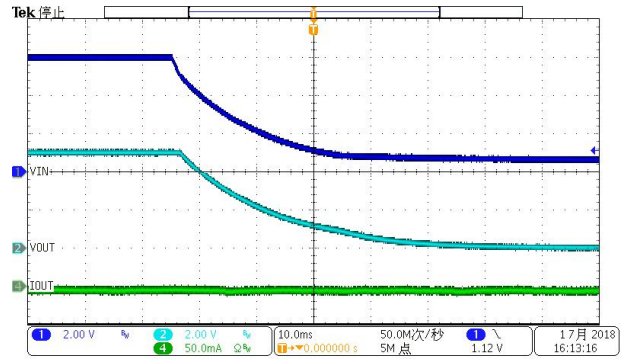
$V_{OUT}=5V, R_2=100k\Omega, C_P=22pF, EN\ OFF$



$V_{OUT}=5V, R_2=100k\Omega, C_P=22pF, Power\ ON$

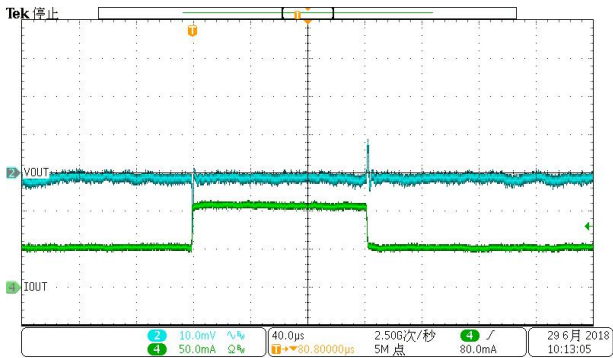


$V_{OUT}=5V, R_2=100k\Omega, C_P=22pF, Power\ OFF$

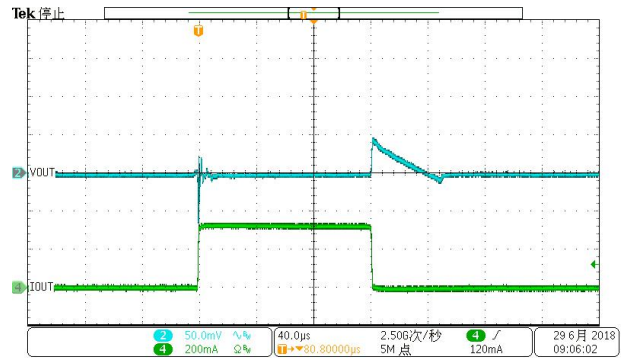


## 2. Load Transient

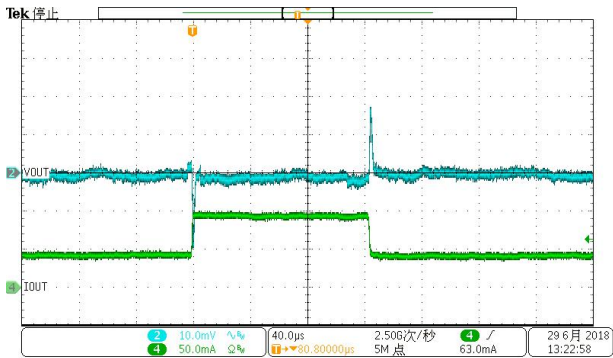
$V_{OUT}=0.8V, I_{OUT}=50-100mA$



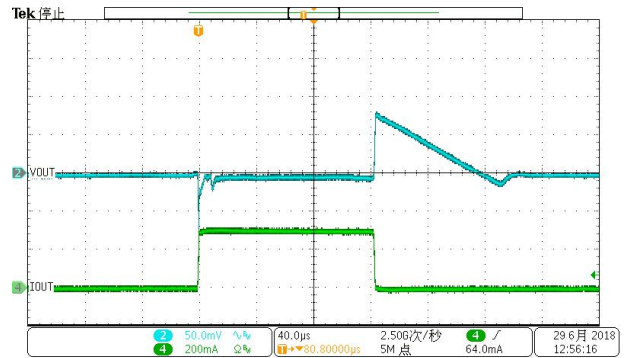
$V_{OUT}=0.8V, I_{OUT}=1-300mA$



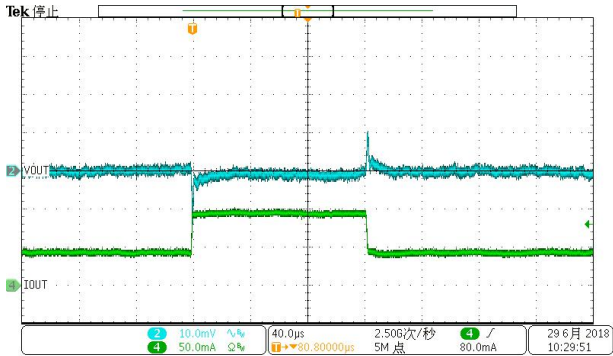
$V_{OUT}=3V, R_2=100k\Omega, C_P=0, I_{OUT}=50-100mA$



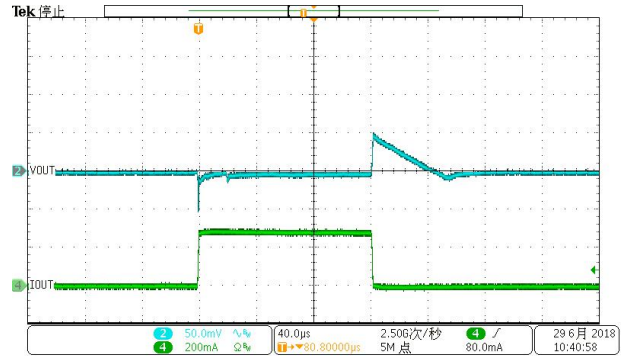
$V_{OUT}=3V, R_2=100k\Omega, C_P=0, I_{OUT}=1-300mA$



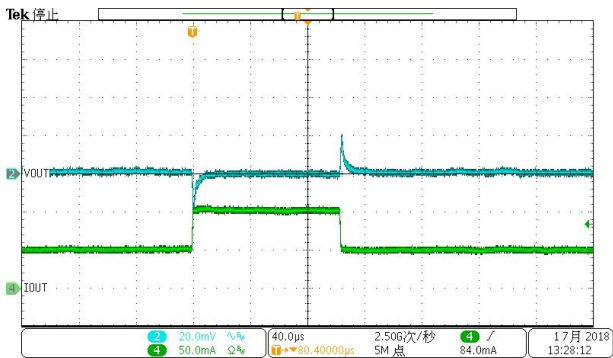
$V_{OUT}=3V, R_2=100k\Omega, C_P=22pF, I_{OUT}=50-100mA$



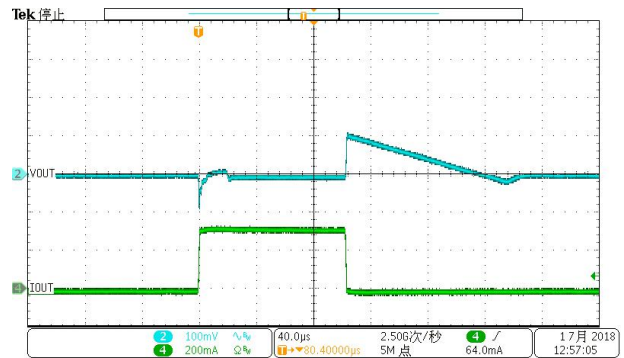
$V_{OUT}=3V, R_2=100k\Omega, C_P=22pF, I_{OUT}=1-300mA$



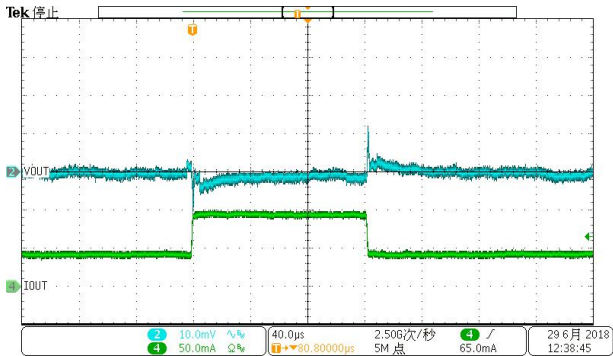
$V_{OUT}=5V, R_2=100k\Omega, C_P=0, I_{OUT}=50-100mA$



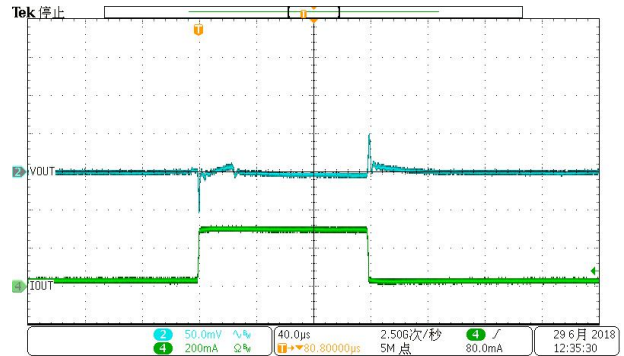
$V_{OUT}=5V, R_2=100k\Omega, C_P=0, I_{OUT}=1-300mA$



$V_{OUT}=5V, R_2=100k\Omega, C_P=22pF, I_{OUT}=50-100mA$

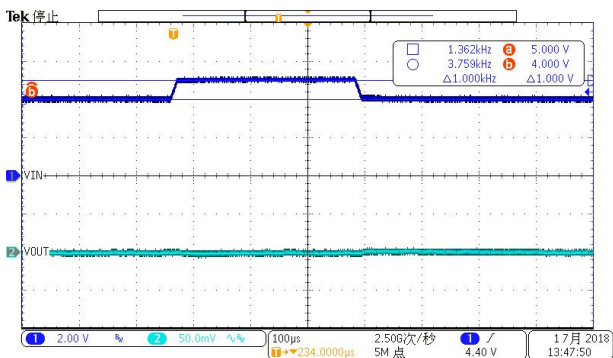


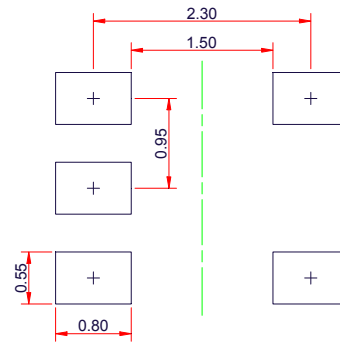
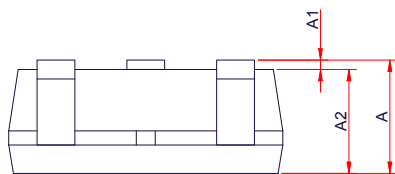
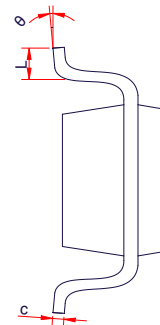
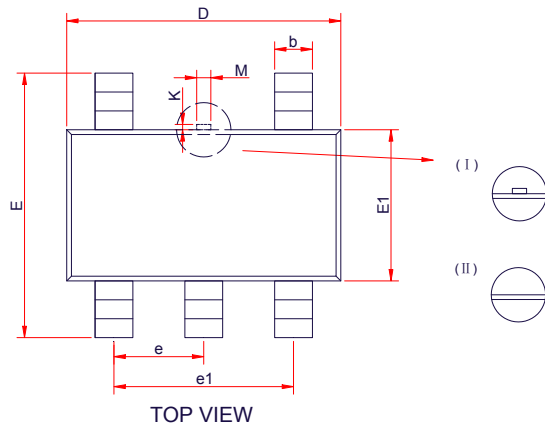
$V_{OUT}=5V, R_2=100k\Omega, C_P=22pF, I_{OUT}=1-300mA$



### 3. Line Transient

$V_{IN}=4V-5V, I_{OUT}=1mA$

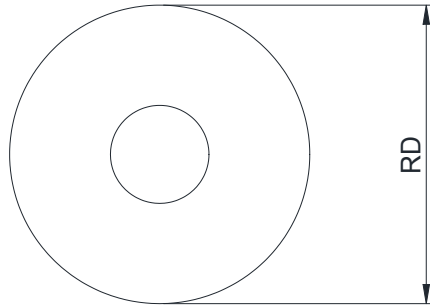


**PACKAGE OUTLINE DIMENSIONS**
**SOT-23-5L**

**SIDE VIEW**
**RECOMMENDED LAND PATTERN (unit: mm)**

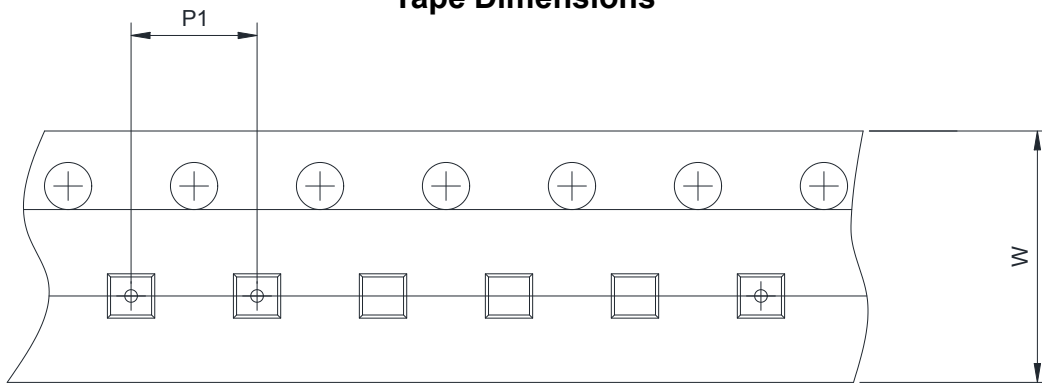
Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	-	-	1.45
A1	0.00	-	0.15
A2	0.90	1.10	1.30
b	0.30	0.40	0.50
c	0.10	-	0.21
D	2.72	2.92	3.12
E	2.60	2.80	3.00
E1	1.40	1.60	1.80
e	0.95 BSC		
e1	1.90 BSC		
L	0.30	0.45	0.60
M	0.10	0.15	0.25
K	0.00	-	0.25
θ	0°	-	8°

**TAPE AND REEL INFORMATION**

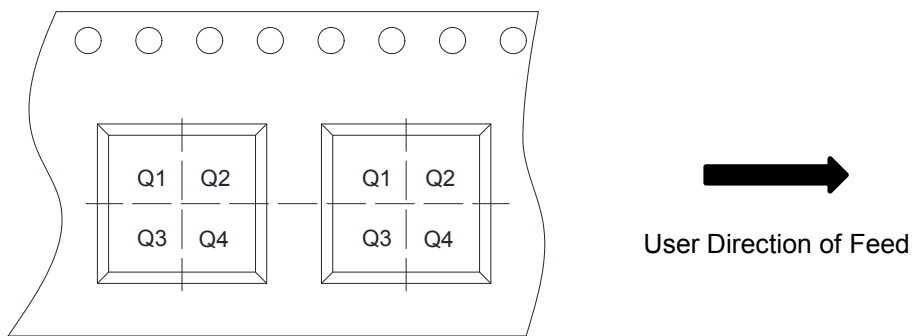
**Reel Dimensions**



**Tape Dimensions**



**Quadrant Assignments For PIN1 Orientation In Tape**



RD	Reel Dimension	<input checked="" type="checkbox"/> 7inch	<input type="checkbox"/> 13inch
W	Overall width of the carrier tape	<input checked="" type="checkbox"/> 8mm	<input type="checkbox"/> 12mm <input type="checkbox"/> 16mm
P1	Pitch between successive cavity centers	<input type="checkbox"/> 2mm	<input checked="" type="checkbox"/> 4mm <input type="checkbox"/> 8mm
Pin1	Pin1 Quadrant	<input type="checkbox"/> Q1	<input type="checkbox"/> Q2 <input checked="" type="checkbox"/> Q3 <input type="checkbox"/> Q4