OmniVision’s OX08B4C is a front-view, 8.3MP image sensor that features the automotive industry’s best HDR of 140 dB and adds a new benchmark in LED flicker mitigation (LFM) performance, enabled by the sensor’s on-chip HALE (HDR and LFM engine) combination algorithm. The optimal clarity and range offered by this sensor, in combination with integrated ASIL-C features, is vital for front-view automotive applications, such as ADAS and level 3+ autonomous vehicles, where the ability to accurately detect people and objects from far away in all lighting conditions is safety-critical.

The OX08B4C utilizes OmniVision’s dual conversion gain (DCG) technology to achieve 82 dB dynamic range on the first exposure, whereas competitors’ image sensors only provide a dynamic range of 60 dB or less. Unlike DCG, the competing method, known as staggered HDR, relies on additional passes that introduce motion artifacts and diminish range, especially in low light. Additionally, our 3D stacking technology allowed us to integrate our unique HALE combination algorithm into the OX08B4C.

With OmniVision’s 4-cell technology, we can offer the industry’s best LFM with optimal low-light performance in a 1/1.73” optical format that has a 2.1 micron pixel pitch. Furthermore, our PureCell®Plus-S stacked architecture enables each pixel to perform optimally via higher full-well capacity for improved HDR. Additionally, the OX08B4C has region-of-interest functionality for even clearer images of moving objects within fixed areas in the camera’s field of view. To ensure the safety of images being used for machine vision applications, ultra low power cybersecurity is also integrated using industry-standard encryption techniques and consuming less than 10% of the sensor’s total power budget.

The OX08B4C offers 4-capture, 3840 x 2160 resolution at 36 fps and a 16:9 display aspect ratio. It will be available in different color-filter patterns to match the leading machine vision applications in the industry, and is planned to be AEC-Q100 Grade 2 certified.

Find out more at www.ovt.com.
### Product Features
- **image size:** 3840 x 2160 and any cropped size equal or larger than VGA
- **PureCel® Plus-S technology**
- **high dynamic range**
- **high sensitivity**
- **image signal processor functions:**
  - defect pixel correction
  - HDR combination
  - PWL compression, etc.
- **SCCB for register programming**
- **dedicated safety features** to fulfill ASIL C hardness metrics
- **programmable GPIOs**
- **high speed serial data transfer** with MIPI CSI-2
- **external frame synchronization capability**
- **embedded temperature sensor**
- **one-time programmable (OTP) memory**
- **support for LED flicker mitigation (LFM) function**
- **cybersecurity for camera/host interface hacking prevention**

### Product Specifications
- **active array size:** 3840 x 2160
- **power supply:**
  - analog: 2.9V
  - digital: 1.1V
  - I/O pads: 1.8V/3.3V
- **power requirements:**
  - active: streaming @4K36
  - 743 mW typical (4 captures), 634 mW typical (3 captures)
- **temperature range:**
  - operating: -40°C to +105°C sensor ambient temperature and -40°C to +125°C junction temperature
- **output interfaces:**
  - up to 4-lane MIPI CSI-2
- **input clock frequency:** 1B - 27 MHz
- **lens size:** 1/1.73''
- **lens chief ray angle:** 18°
- **SCCB speed:** up to 1 MHz
- **scan mode:** progressive
- **shutter:** rolling shutter
- **output formats:** unconverted 24-bit, 20/16/14/12-bit (PWL) combined HDR
- **maximum image transfer rate:** up to 4K.36 fps
- **sensitivity:** 19,500 e-/lux-sec (clear pixel response with 5100K illumination)
- **max S/N ratio:** 40.6 dB
- **dynamic range:** 120 dB (DCG + VS), 140 dB (DCG + VS + SPD)
- **pixel size:** 2.1 µm x 2.1 µm
- **image area array:**
  - 8097.6 µm x 4569.6 µm
- **package cover glass type:** double sided anti-reflective (AR/AR) coating (without IRF)
- **package dimensions:**
  - a-BGA®: 11 mm x 8 mm

### Ordering Information
- **OX08B4C**
- **86-pin a-BGA™ packed in tray without protective film, engineering special control grade (alpha sample)